





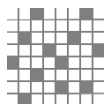


DLP® Modules Overview



DLP® Module	V-7000	V-7001	V-6501	V-9501	V-9601	V-9001
DLP Product Family	Discovery™	Discovery™	Discovery™	Discovery™	Discovery™	Discovery™
DLP Chip						
Controller Hardware Produced by	V4100 ViALUX	V4395 ViALUX	V4390 ViALUX	V4395 ViALUX	V4395 ViALUX	V4390 ViALUX
Controller Software	ALP-4.2 API	ALP-4.3 API	ALP-4.3 API	ALP-4.3 API	ALP-4.3 API	ALP-4.3 API
Controller Interface	USB2.0 HighSpeed	USB3.0 SuperSpeed				
# of 8-bit frames # of 1-bit frames In on-board RAM	5 461 43 690 DDR2	10 922 87 380 DDR3	4 142 33 140 DDR3	4 142 33 140 DDR3	3 728 29 826 DDR3	2 097 16 776 DDR3
USB upload, 1-bit PC → on-board RAM	> 800* fps	> 4000* fps	> 1600* fps	> 1600* fps	> 1500* fps	> 1100* fps
Frame rate, 1-bit On-board RAM → DMD	22 727 fps	22 727 fps	10 309 fps	17 857 fps	16 393 fps	12 987 fps
DMD reset mode	global					
DMD reset trigger programmable	master/slave delay, pulse					
DMD aspect ratio	16:12	16:12	16:9	16:9	16:10	16:10
DMD mirror pitch	13.7 μm	13.7 μm	7.6 μm	10.8 μm	10.8 μm	7.6 μm
DMD mirrors / row	1024	1024	1920	1920	1920	2560
DMD rows / array	768	768	1080	1080	1200	1600
DMD window option Wavelength [nm]	VIS:400-700** UV: 355-400	VIS:400-700** UV: 355-400	VIS:400-700**	VIS:400-700** UV: 355-400	VIS:400-700**	VIS:400-700**
* typical value, can vary depending upon data compression ratio and PC ** up to 2500 nm with reduced efficiency						





Contact: dlp@vialux.de

ViALUX GmbH
Am Erlenwald 10
09128 Chemnitz
Germany

phone: +49 (0) 371 33 42 47 0
fax: +49 (0) 371 33 42 47 10
web: www.vialux.de